EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	137	((reverse opposite) with (PCB CB board) same (backup (back adj up)))	US-PGPUB; USPAT	OR	ON	2008/03/07 17:24
S2	99	((reverse opposite) with (PCB CB board) same (backup (back adj up))) and ((mount$4 plac$7) with (PCB CB board))	US-PGPUB; USPAT	OR	ON	2008/03/07 17:26
S3	5	(US-20030058624-$).did. or (US-6549418-$ or US-6540527-$ or US-5711065-$ or US-5218753-$).did.	US-PGPUB; USPAT	OR	OFF	2008/03/09 14:48
S4	0	S3 and display$4	US-PGPUB; USPAT	OR	ON	2008/03/09 14:49
S5	246	((mount$7 plac$7) with (PCB CB board)) and ((computer with aided with design) CAD) and ((back adj up) backup)	US-PGPUB; USPAT	OR	ON	2008/03/20 13:21
S6	20	((mount$7 plac$7) with (PCB CB board)) and ((computer with aided with design) CAD) and ((back adj up) backup) and "716"/$	US-PGPUB; USPAT	OR	ON	2008/03/20 13:41
S7	31	((mount$7 plac$7) with (PCB CB board)) and ((computer with aided with design) CAD) and ((back adj up) backup) and ("716"/$ "703"/$ "700"/$)	US-PGPUB; USPAT	OR	ON	2008/03/20 13:43

S8	141	((mount$7 plac$7) with (PCB CB board)) and (((computer with aided with design) CAD) and (pin with (PCB CB board)) and ("716"/$ "703"/$ "700"/$)	US-PGPUB; USPAT	OR	ON	2008/03/20 14:15
S9	71	((mount$7 plac$7) with (PCB CB board)) and (((computer with aided with design) CAD) and (pin with (PCB CB board) with (position plac$7 locat $7)) and ("716"/$ "703"/$ "700"/$)	US-PGPUB; USPAT	OR	ON	2008/03/20 14:32
S10	64	((mount$7 plac$7) with (PCB CB board)) and (((computer with aided with design) CAD) and (pin with (PCB CB board) with (position plac$7 locat $7)) and ("716"/$ "703"/$ "700"/$) and ((top bottom side) with (PCB CB board))	US-PGPUB; USPAT	OR	ON	2008/03/20 14:39
S11	80	((mount$7 plac$7) with (PCB CB board)) and (((computer with aided with design) CAD) and (((pin support) with (PCB CB board) with (position plac$7 locat $7)) and ("716"/$ "703"/$ "700"/$) and ((top bottom side) with (PCB CB board))	US-PGPUB; USPAT	OR	ON	2008/03/20 14:54

S12	80	((mount$7 plac$7) with (PCB CB board)) and ((computer with aided with design) CAD) and ((pin support) with (PCB CB board) with (position plac$7 locat $7)) and ("716"/$ "703"/$ "700"/$) and ((top bottom side) with (PCB CB board))	US-PGPUB; USPAT	OR	ON	2008/03/20 16:33
S13	22	((mount$7 plac$7) with (PCB CB board)) and ((computer with aided with design) CAD) and ((pin support) with (position plac$7 locat $7 coordinate)) and ("716"/$ "703"/$ "700"/$) and ((top bottom side) with (PCB CB board)) not S12	US-PGPUB; USPAT	OR	ON	2008/03/20 16:33
S14	1	((mount$7 plac$7) with (PCB CB board)) and ((computer with aided with design) CAD) and ("716"/$ "703"/$ "700"/$) and ((top bottom side) with (PCB CB board)) and ((backup (back adj up)) with (PCB CB board))	US-PGPUB; USPAT	OR	ON	2008/03/20 16:44
S15	2353	((backup (back adj up)) with (PCB CB board))	US-PGPUB; USPAT	OR	ON	2008/03/20 16:46
S16	37	((mount$7 plac$7) with (PCB CB board)) and ((computer with aided with design) CAD) and ((backup (back adj up)) with (PCB CB board))	US-PGPUB; USPAT	OR	ON	2008/03/20 16:46

S17	102	((mount$7 plac$7) with (PCB CB board)) and (((computer with aided with design) CAD) and ((pin support) with (position plac$7 locat $7 coordinate)) and ("716"/$ "703"/$ "700"/$) and ((top bottom side) with (PCB CB board))	US-PGPUB; USPAT	OR	ON	2008/03/20 16:47
S18	106	((mount$7 plac$7) with (PCB CB board)) and (((computer with aided with design) CAD) and ((pin support$4) with (position plac$7 locat $7 coordinate)) and ("716"/$ "703"/$ "700"/$) and ((top bottom side) with (PCB CB board))	US-PGPUB; USPAT	OR	ON	2008/03/20 16:47
S19	1	((Oohashi with Teruyuki) (Kurashina with Takashi)).in.	US-PGPUB; USPAT	OR	ON	2008/03/20 16:54
S20	327	(Fuji with Machine). as.	US-PGPUB; USPAT	OR	ON	2008/03/20 16:55
S21	15	(Fuji with Machine). as. and (backup with device)	US-PGPUB; USPAT	OR	ON	2008/03/20 16:55
S22	84	((mount$7 plac$7) with (PCB CB board)) and (((computer with aided with design) CAD) and ((pin support$4) with (PCB CB board) with (position plac$7 locat $7 coordinate)) and ("716"/$ "703"/$ "700"/$) and ((top bottom side) with (PCB CB board))	US-PGPUB; USPAT	OR	ON	2008/03/20 17:00

S23	90	((mount$7 plac$7) with (PCB CB board)) and ((computer with aided with design) CAD) and ((pin support$4 hold$4) with (PCB CB board) with (position plac $7 locat$7 coordinate)) and ("716"/$ "703"/$ "700"/$) and ((top bottom side) with (PCB CB board))	US-PGPUB; USPAT	OR	ON	2008/03/20 17:00
S24	409	((mount$7 plac$7) with (PCB CB board)) and ((computer with aided with design) CAD) and ((pin support$4 hold$4) with (PCB CB board) with (position plac $7 locat$7 coordinate)) and ((top bottom side) with (PCB CB board))	US-PGPUB; USPAT	OR	ON	2008/03/20 17:07
S25	319	((mount$7 plac$7) with (PCB CB board)) and ((computer with aided with design) CAD) and ((pin support$4 hold$4) with (PCB CB board) with (position plac $7 locat$7 coordinate)) and ((top bottom side) with (PCB CB board)) not S23	US-PGPUB; USPAT	OR	ON	2008/03/20 17:07

S26	93	((mount$7 plac$7) with (PCB CB board)) and (((computer with aided with design) CAD) and ((pin support$4 hold$4 handl$4) with (PCB CB board) with (position plac$7 locat $7 coordinate)) and ((top bottom side) with (PCB CB board)) and ("700"/ $ "716"/$ "703"/$)	US-PGPUB; USPAT	OR	ON	2008/03/20 17:12
S27	428	((mount$7 plac$7) with (PCB CB board)) and ((computer with aided with design) CAD) and ((pin support$4 hold$4 handl$4) with (PCB CB board) with (position plac$7 locat $7 coordinate)) and ((top bottom side) with (PCB CB board))	US-PGPUB; USPAT	OR	ON	2008/03/20 17:13
S28	335	((mount$7 plac$7) with (PCB CB board)) and ((computer with aided with design) CAD) and ((pin support$4 hold$4 handl$4) with (PCB CB board) with (position plac$7 locat $7 coordinate)) and ((top bottom side) with (PCB CB board)) not S26	US-PGPUB; USPAT	OR	ON	2008/03/20 17:14
S29	0	(Yoshiro adj Yasui)	JPO	OR	ON	2008/03/20 17:21
S30	15	(Yoshihiro with Yasui).in.	JPO	OR	ON	2008/03/20 17:21
S31	17	(Yoshihiro with Yasui).in.	USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/03/20 17:23

S32	18	((mount$7 plac$7) with (PCB CB board)) and ((computer with aided with design) CAD) and (pin with (support$4 hold$4 handl$4) with (PCB CB board) with (position plac$7 locat $7 coordinate)) and ((top bottom side) with (PCB CB board))	US-PGPUB; USPAT	OR	ON	2008/03/20 17:34
S33	2	((mount$7 plac$7) with (PCB CB board)) and ((computer with aided with design) CAD) and (pin with interference with (component part)) and ((top bottom side) with (PCB CB board))	US-PGPUB; USPAT	OR	ON	2008/03/20 17:47
S34	327	(Fuji with machine). as.	US-PGPUB; USPAT	OR	ON	2008/03/20 17:49
S35	5	(Fuji with machine). as. and ((mount$7 plac$7) with (PCB CB board)) and ((computer with aided with design) CAD)	US-PGPUB; USPAT	OR	ON	2008/03/20 17:49
S36	45	((mount$7 plac$7) with (PCB CB board)) and ((computer with aided with design) CAD) and (pin with (support$4 hold$4 handl$4) with (position plac$7 locat $7 coordinate)) and ((top bottom side) with (PCB CB board))	US-PGPUB; USPAT	OR	ON	2008/03/20 17:58

S37	1179	((mount$7 plac$7) with (PCB CB board)) and ((computer with aided with design) CAD display$4) and (pin with (support$4 hold$4 handl$4) with (position plac $7 locat$7 coordinate)) and ((top bottom side) with (PCB CB board))	US-PGPUB; USPAT	OR	ON	2008/03/20 17:59
S38	403	((mount$7 plac$7) with (PCB CB board)) and ((computer with aided with design) CAD) and (pin with (position plac$7 locat $7 coordinate)) and ((top bottom side) with (PCB CB board))	US-PGPUB; USPAT	OR	ON	2008/03/20 18:07
S39	273	((mount$7 plac$7) with (PCB CB board)) and ((computer with aided with design) CAD) and (pin with (position plac$7 locat $7 coordinate) same (PCB CB board)) and ((top bottom side) with (PCB CB board))	US-PGPUB; USPAT	OR	ON	2008/03/20 18:08
S40	65	((mount$7 plac$7) with (PCB CB board)) and ((computer with aided with design) CAD) and (pin with (position plac$7 locat $7 coordinate) same (PCB CB board)) and ((top bottom side) with (PCB CB board)) and ("703"/ $ "716"/$ "700"/$)	US-PGPUB; USPAT	OR	ON	2008/03/20 18:09

S41	10	((computer with aided with design) CAD) and (pin with (position plac$7 locat $7 coordinate) with (PCB CB board) with support) and ((top bottom side) with (PCB CB board))	US-PGPUB; USPAT	OR	ON	2008/03/20 18:19
S42	14	((computer with aided with design) CAD) and (pin with (position plac$7 locat $7 coordinate) with (PCB CB board) with support$4) and (((top bottom side) with (PCB CB board))	US-PGPUB; USPAT	OR	ON	2008/03/20 18:19
S43	30	((computer$7 with design$4) CAD) and (pin with (position plac$7 locat$7 coordinate) with (PCB CB board) with support$4) and (((top bottom side) with (PCB CB board))	US-PGPUB; USPAT	OR	ON	2008/03/20 18:20
S44	25	((computer$7 with design$4) CAD) and (pin with (position plac$7 locat$7 coordinate) with (PCB CB board) with support$4) and (((top bottom side) with (PCB CB board)) not S40	US-PGPUB; USPAT	OR	ON	2008/03/20 18:21
S45	13196	((mount$7 plac$7) with (PCB CB board)) and (superpos$4 superimpos$4)	US-PGPUB; USPAT	OR	ON	2008/03/21 09:25
S46	1116	((mount$7 plac$7) with (PCB CB board)) and (superpos$4 superimpos$4) and ((computer$7 with design$4) CAD)	US-PGPUB; USPAT	OR	ON	2008/03/21 09:26

S47	1917	((mount$7 plac$7) with (PCB CB board)) and (superpos$4 superimpos$4 overlay$4) and ((computer$7 with design$4) CAD)	US-PGPUB; USPAT	OR	ON	2008/03/21 09:40
S48	487	((mount$7 plac$7) with (PCB CB board)) and (superpos$4 superimpos$4 overlay$4) and ((computer$7 with design$4) CAD) and (pin with (position locat$4 coordinate plac$7))	US-PGPUB; USPAT	OR	ON	2008/03/21 09:48
S49	33	((mount$7 plac$7) with (PCB CB board)) and (superpos$4 superimpos$4 overlay$4) and ((computer$7 with design$4) CAD) and (pin with (position locat$4 coordinate plac$7)) and ("700"/$ "716"/$ "703"/$)	US-PGPUB; USPAT	OR	ON	2008/03/21 09:54
S50	83	((mount$7 plac$7) with (PCB CB board)) and ((superpos$4 superimpos$4 overlay$4) same (PCB CB board)) and ((computer$7 with design$4) CAD) and (pin with (position locat$4 coordinate plac$7))	US-PGPUB; USPAT	OR	ON	2008/03/21 10:14

S51	123	((mount$7 plac$7) with (PCB CB board)) and ((superpos$4 superimpos$4 overlay$4) same (PCB CB board)) and ((computer$7 with (design$4 display$4)) CAD) and (pin with (position locat$4 coordinate plac$7))	US-PGPUB; USPAT	OR	ON	2008/03/21 10:26
S52	290	((mount$7 plac$7) with (PCB CB board)) and ((superpos$4 superimpos$4 overlay$4) same (PCB CB board)) and ((computer$7 with (design$4 display$4)) CAD) and ((pin hole) with (position locat$4 coordinate plac$7))	US-PGPUB; USPAT	OR	ON	2008/03/21 10:29
S53	167	S52 not S51	US-PGPUB; USPAT	OR	ON	2008/03/21 10:58
S54	1758	((mount$7 plac$7) with (PCB CB board)) and ((superpos$4 superimpos$4 overlay$4) same (PCB CB board)) and ((pin hole) with (position locat$4 coordinate plac$7)) not S52	US-PGPUB; USPAT	OR	ON	2008/03/21 11:25
S55	563	((mount$7 plac$7) with (PCB CB board)) and ((superpos$4 superimpos$4 overlay$4) same (PCB CB board) same side) and ((pin hole) with (position locat$4 coordinate plac$7)) not S52	US-PGPUB; USPAT	OR	ON	2008/03/21 11:26

S56	821	((mount$7 plac$7) with (PCB CB board)) and ((superpos$4 superimpos$4 overlay$4) same (PCB CB board) same ($7side top bottom)) and ((pin hole) with (position locat$4 coordinate plac$7)) not S52	US-PGPUB; USPAT	OR	ON	2008/03/21 11:27
S57	1053	((mount$7 plac$7) with (PCB PWB CB board card)) and ((superpos$4 superimpos$4 overlay$4) same (PCB CB board card) same ($7side top bottom)) and ((pin hole) with (position locat$4 coordinate plac$7)) not S52	US-PGPUB; USPAT	OR	ON	2008/03/21 11:31
S58	443	((mount$7 plac$7) with (PCB PWB CB board card)) and ((superpos$4 superimpos$4 overlay$4) same (PCB CB board card) same ($7side top bottom)) and ((pin hole) with (position locat$4 coordinate plac$7)) and (computer$7 display $7) not S52	US-PGPUB; USPAT	OR	ON	2008/03/21 11:32
S59	447	((mount$7 plac$7) with (PCB PWB CB board card)) and ((superpos$4 superimpos$4 overlay$4) same (PCB PWB CB board card) same ($7side top bottom)) and ((pin hole) with (position locat$4 coordinate plac$7)) and (computer$7 display$7) not S52	US-PGPUB; USPAT	OR	ON	2008/03/21 11:32

S60	1210	((mount$7 plac$7) with (PCB PWB CB board card)) and (((superpos$4 superimpos$4 overla $6) same (PCB PWB CB board card) same ($7side top bottom)) and ((pin hole) with (position locat$4 coordinate plac$7)) and (computer$7 display $7) not S52	US-PGPUB; USPAT	OR	ON	2008/03/21 12:06
S61	763	S60 not (S52 S59)	US-PGPUB; USPAT	OR	ON	2008/03/21 12:15
S62	563	((mount$7 plac$7) with (PCB PWB CB board card)) and (((superpos$4 superimpos$4 overla $6) same (PCB PWB CB board card) same ($7side top bottom)) and ((pin hole) with (position locat$4 coordinate plac$7)) and ((computer$7 display $7) same (PCB PWB CB board card)) not (S52 S59)	US-PGPUB; USPAT	OR	ON	2008/03/21 12:17
S63	397	((mount$7 plac$7) with (PCB PWB CB board card)) and (((superpos$4 superimpos$4 overla $6) same (PCB PWB CB board card) same ($7side top bottom)) and ((pin hole) with (position locat$4 coordinate plac$7)) and ((computer$7 display $7) same (PCB PWB CB board card)) not (S52 S59) and ((wiring circuit) adj board)	US-PGPUB; USPAT	OR	ON	2008/03/21 12:18

S64	3212	(prevent$4 with (bend$4 flex$4) with (PCB PWB CB board card))	US-PGPUB; USPAT	OR	ON	2008/03/21 12:30
S65	951	(prevent$4 with (bend$4 flex$4) with (PCB PWB CB board card)) and ((pin hole support) with (position locat $4 coordinate plac $7) with (PCB PWB CB board card))	US-PGPUB; USPAT	OR	ON	2008/03/21 12:33
S66	70	(prevent$4 with (bend$4 flex$4) with (PCB PWB CB board card)) and ((pin hole support) with (position locat $4 coordinate plac $7) with (PCB PWB CB board card)) and (precis$4 with (mount$4 plac$5))	US-PGPUB; USPAT	OR	ON	2008/03/21 12:36
S67	46	(prevent$4 with (bend$4 flex$4) with (PCB PWB CB board card) same (pin hole support)) and ((pin hole support) with (position locat$4 coordinate plac$7) with (PCB PWB CB board card)) and (precis$4 with (mount$4 plac$5))	US-PGPUB; USPAT	OR	ON	2008/03/21 12:54
S68	951	(prevent$4 with (bend$4 flex$4) with (PCB PWB CB board card)) and ((pin hole support) with (position locat $4 coordinate plac $7) with (PCB PWB CB board card))	US-PGPUB; USPAT	OR	ON	2008/03/21 13:01

S69	531	(prevent$4 with (bend$4 flex$4) with (PCB PWB CB board card) same (pin hole support)) and ((pin hole support) with (position locat$4 coordinate plac$7) with (PCB PWB CB board card))	US-PGPUB; USPAT	OR	ON	2008/03/21 13:02
S70	571	(prevent$4 with (bend$4 flex$4 bow $4) with (PCB PWB CB board card) same (pin hole support)) and ((pin hole support) with (position locat$4 coordinate plac$7) with (PCB PWB CB board card))	US-PGPUB; USPAT	OR	ON	2008/03/21 13:03
S71	450	(prevent$4 with (bend$4 flex$4 bow $4) with (PCB PWB CB board card) same (pin hole support)) and ((pin hole support) with (position locat$4 coordinate plac$7) with (PCB PWB CB board card)) and ((circuit wiring) adj board)	US-PGPUB; USPAT	OR	ON	2008/03/21 13:11
S72	6	(prevent$4 with (bend$4 flex$4 bow $4) with (PCB PWB CB board card) same (pin hole support)) and (determin$7 with (pin hole support) with (position locat $4 coordinate plac $7) with (PCB PWB CB board card)) and ((circuit wiring) adj board)	US-PGPUB; USPAT	OR	ON	2008/03/21 13:47

S73	72	(prevent$4 with (bend$4 flex$4 bow $4) with (PCB PWB CB board card)) and (spacer with (position locat$4 coordinate plac$7) with (PCB PWB CB board card)) and ((circuit wiring) adj board)	US-PGPUB; USPAT	OR	ON	2008/03/21 13:56
S74	19	(prevent$4 with (bend$4 flex$4 bow $4) with (PCB PWB CB board card) same spacer) and (spacer with (position locat$4 coordinate plac$7) with (PCB PWB CB board card)) and ((circuit wiring) adj board)	US-PGPUB; USPAT	OR	ON	2008/03/21 14:01
S75	19	((mount$7 plac$7) with (PCB PWB CB board card)) and ((superpos$4 superimpos$4 overlay$4) same (PCB PWB CB board card) same ($7side top bottom) same spacer) and (spacer with (position locat $4 coordinate plac $7) with (PCB PWB CB board card)) and (computer$7 display $7 CAD)	US-PGPUB; USPAT	OR	ON	2008/03/21 15:06
S76	6	((mount$7 plac$7) with (PCB PWB CB board card)) and ((superpos$4 superimpos$4 overlay$4) same (PCB PWB CB board card) same ($7side top bottom)) and (spacer with (position locat$4 coordinate plac$7) with (PCB PWB CB	US-PGPUB; USPAT	OR	ON	2008/03/21 15:10

		board card)) and ((computer$7 with design$7) CAD)				
S77	9	((mount$7 plac$7) with (PCB PWB CB board card)) and ((superpos$4 superimpos$4 overlay$4) same (PCB PWB CB board card) same ($7side top bottom)) and (spacer with (position locat$4 coordinate plac$7) same (PCB PWB CB board card)) and ((computer$7 with design$7) CAD)	US-PGPUB; USPAT	OR	ON	2008/03/21 15:11
S78	9	((superpos$4 superimpos$4 overlay$4) same (PCB PWB CB board card) same ($7side top bottom)) and (spacer with (position locat$4 coordinate plac$7) same (PCB PWB CB board card)) and ((computer$7 with design$7) CAD)	US-PGPUB; USPAT	OR	ON	2008/03/21 15:12
S79	33	((superpos$4 superimpos$4 overlay$4) same (PCB PWB CB board card) same ($7side top bottom)) and (spacer with (position locat$4 coordinate plac$7) same (PCB PWB CB board card)) and display$4	US-PGPUB; USPAT	OR	ON	2008/03/21 15:13

S80	87	((superpos$4 superimpos$4 overlay$4) same (PCB PWB CB board card) same ($7side top bottom)) and (spacer with (position locat$4 coordinate plac$7) same (PCB PWB CB board card))	US-PGPUB; USPAT	OR	ON	2008/03/21 15:14
S81	157	((superpos$4 superimpos$4 overla $6) same (PCB PWB CB board card) same ($7side top bottom)) and (spacer with (position locat$4 coordinate plac$7) with (PCB PWB CB board card))	US-PGPUB; USPAT	OR	ON	2008/03/21 15:17
S82	1	(spacer with (position locat$4 coordinate plac$7) with (PCB PWB CB board card)) and (superimpos$4 superpos$4) and CAD	US-PGPUB; USPAT	OR	ON	2008/03/21 15:20
S83	18	(support with (position locat$4 coordinate plac$7) with (PCB PWB CB board card)) and (superimpos$4 superpos$4) and CAD	US-PGPUB; USPAT	OR	ON	2008/03/21 15:21
S84	3	(support with (position locat$4 coordinate plac$7) with (PCB PWB CB board card)) and ((superimpos$4 superpos$4) with image with (PCB PWB CB board card)) and ((wiring circuit) adj board)	US-PGPUB; USPAT	OR	ON	2008/03/21 15:30

S85	88	((position locat$4 coordinate plac$7) with (PCB PWB CB board card)) and ((superimpos$4 superpos$4) with image with (PCB PWB CB board card)) and ((wiring circuit) adj board)	US-PGPUB; USPAT	OR	ON	2008/03/21 15:32

3/21/08 5:34:00 PM
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